

ABSTRACT OF THE DISCLOSURE

There is provided a resistor unit 115 for bypassing an output cell 107 of an interrupt request signal and an input
5 cell 108 of an external interrupt factor, and the output cell 107 of the interrupt request signal is controlled to a high impedance when an internal power source is broken. Thus, an interrupt request signal sent from the external interrupt factor is given to a CPU when the internal power source is broken.